

Notice of References Cited	Application/Control No. 09/661,912	Applicant(s)/Patent Under Reexamination AZIMI, SAEED	
	Examiner Justin I. King	Art Unit 2111	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-5,133,060	07-1992	Weber et al.	711/113
	C	US-5,218,686	06-1993	Thayer, John S.	711/100
	D	US-5,544,334	08-1996	Noll, Mark G.	710/306
	E	US-6,407,913	06-2002	Peachey et al.	361/685
	F	US-6,757,698	06-2004	McBride et al.	707/204
	G	US-5,802,554	09-1998	Caceres et al.	711/103
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Tiberiu Chelcea and Steven Nowick, "A Low-Latency FIFO for Mixed-Clock Systems", 2000, IEEE, VLSI, 2000 Proceedings, IEEE Computer Society Workshop.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
 Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.